Physics of Electronics: 7. Junction Diodes

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Pn junction with forward bias

• Consider a junction biased with a voltage V:



Same Fermi energy at both sides

Fermi energies separated by eV

Pn junction with forward bias

• Diode equation:



$$J = J_{\rm h} + J_{\rm e} = e \left(\frac{D_{\rm h} p_{\rm n}}{L_{\rm h}} + \frac{D_{\rm e} n_{\rm p}}{L_{\rm e}} \right) \left[\exp(eV/kT) - 1 \right]$$

Pn junction with reversed bias

- Currents through the junction :
 - Applying diode equation (with -V):



Pn junction with reversed bias

- Decrease of minority carriers:
 - As before (with -V):



Pn junction with finite dimensions

- Current in a finite junction:
 - From the continuity eq. we obtained



Depletion-layer capacitance

• The depletion layer forms a capacitance (important for high frequency applications)



Using Poisson relation ($\partial^2 V/\partial x^2 = -\rho/\epsilon$) and continuity for voltage and its derivative:

I:
$$V_1(x) = eN_a x^2/2\epsilon$$

II:
$$V_2 = -eN_d x^2/2\epsilon + C_1 x + C_2$$

 $C_1 = ed_p(N_a + N_d)/\epsilon$ $C_2 = -\frac{ed_p^2(N_a + N_d)}{2\epsilon}$
Applying $\mathscr{E} = 0$ at $x \ge d_n + d_p$: $d_p/d_n = N_d/N_a$

Applying $V = V_0$ at $x \ge d_n + d_p$ & using previous relation:

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$$d_{\rm p} = \left(\frac{2\epsilon V_0 N_{\rm d}}{eN_{\rm a}(N_{\rm a} + N_{\rm d})}\right)^{1/2} \qquad d_{\rm n} = \left(\frac{2\epsilon V_0 N_{\rm d}}{eN_{\rm d}(N_{\rm a} + N_{\rm d})}\right)^{1/2}$$

Depletion-layer capacitance

- Capacitance of the depletion layer:
 - Charge per unit area accumulated at the depletion layer

$$Q_{j} = eN_{d}d_{n} = eN_{a}d_{p}$$
 $Q_{j} = \left(\frac{2\epsilon eV_{j}N_{a}N_{d}}{N_{a} + N_{d}}\right)^{1/2}$

– The capacitance per unit area $(C_j = dQ_j/dV_j)$ is then:



• Reversed biased junction





 r_{np} :Resistance of the junction @ V=0 $C_j(V)$:Depletion layer capacitanceg:Conductance of the reverse flow

• Forward biased junction





- r_{np} : Resistance of the junction @ V=0
- $C_{i}(V)$: Depletion layer capacitance
- \vec{G} : Conductance of the forward flow
- $C_{\rm D}$: Diffusion capacitance

• Forward biased junction



 $\begin{cases} V_{\text{tot}} = V + V_1 \exp(i\omega t) \\ p_{n0} = p_n \exp(eV/kT) \end{cases} \implies p_{n0} = p_n \exp\left(\frac{e}{kT}[V + V_1 \exp(j\omega t)]\right)$

$$P_{n0} = p_n \exp\left(\frac{eV}{kT}\right) \left(1 + \frac{eV_1}{kT} \exp(j\omega t)\right)$$
DC AC

• Therefore we can assume:

$$\delta p = p(x, t) - p_n = p_0(x) + p_1(x) \exp(j\omega t) - p_n$$

DC AC

• Forward biased junction



– Applying continuity equation:



• Forward biased junction



- Alternating currents of holes injected into n-region:

$$J_{hl} = -eD_{h} \left(\frac{\mathrm{d}p(x)}{\mathrm{d}x} \right) \Big|_{x=0} = \frac{(1+i\omega\tau_{Lh})^{1/2}}{L_{h}} \frac{p_{n}D_{h}e^{2}V_{1}}{kT} \exp\left(\frac{eV}{kT}\right) \exp(j\omega t)$$

– Alternating currents of electrons injected into p-region:

$$J_{e1} = -eD_e\left(\frac{\mathrm{d}n(x)}{\mathrm{d}x}\right)\Big|_{x=0} = \frac{(1+\mathrm{i}\,\omega\tau_{\mathrm{Le}})^{1/2}}{L_e}\frac{n_p D_e \,e^2 \,V_1}{kT}\exp\left(\frac{eV}{kT}\right)\exp(\mathrm{j}\omega t)$$

• Forward biased junction



– Total alternating currents of minority injected carriers:

 $J_1 = J_{h1} + J_{e1}$ - Admittance $(Y_1 = J_1/V_1)$ for $\omega \tau \ll 1$: